

School of Engineering and Technology
Department of Electronics and Communication Engineering
Curriculum Feedback Analysis 2025-26

The Department of Electronics and Communication Engineering revises its curriculum for the programmes offered every year based on the relevant trends in industry and emerging technologies by considering the feedback provided by all its stakeholders on the curriculum. This report is an analysis of the feedback collected from the various stakeholders like students, alumni and faculty members and this report shall be forwarded to the Department Curriculum Design and Development Cell (CDC) for consideration while revising the curriculum.

This academic year feedback was collected from a total of 340 students, 29 faculty members, 28 alumni, 6 employers and 11 parents. This feedback was analyzed and this report contains the analysis and recommendations to CDC based on the analysis carried out.

Student Feedback on Curriculum

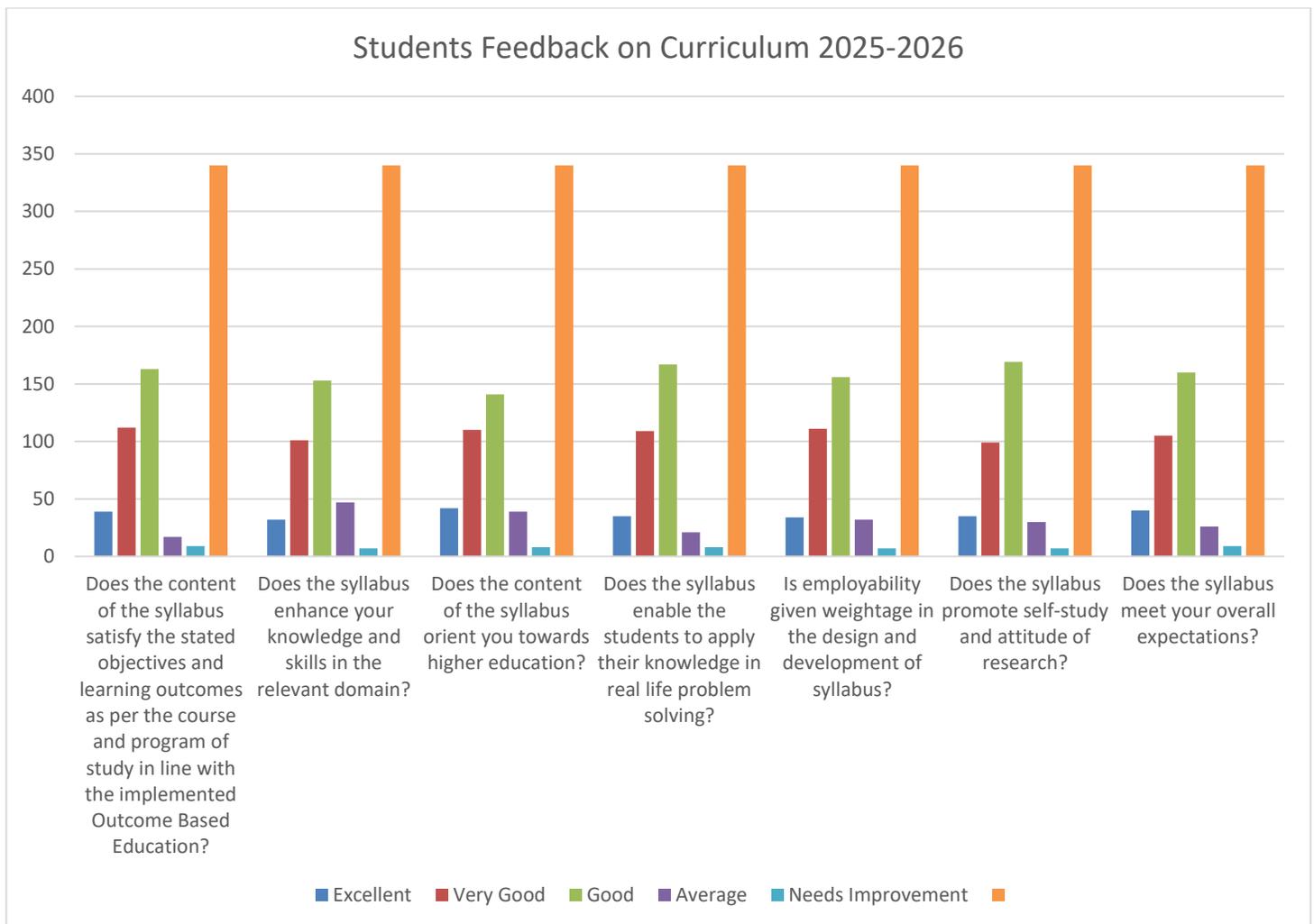
A total of 340 students took the curriculum feedback survey. The questionnaire and the number of responses for each year of study was as follows

All Years of Study					
Total Number of Students Participated in the Survey : 317					
Question	Excellent	Very Good	Good	Average	Needs Improvement
Does the content of the syllabus satisfy the stated objectives and learning outcomes as per the course and program of study in line with the implemented Outcome Based Education?	39	112	163	17	9
Does the syllabus enhance your knowledge and skills in the relevant domain?	32	101	153	47	7
Does the content of the syllabus orient you towards higher education?	42	110	141	39	8



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Does the syllabus enable the students to apply their knowledge in real life problem solving?	35	109	167	21	8
Is employability given weightage in the design and development of syllabus?	34	111	156	32	7
Does the syllabus promote self-study and attitude of research?	35	99	169	30	7
Does the syllabus meet your overall expectations?	40	105	160	26	9



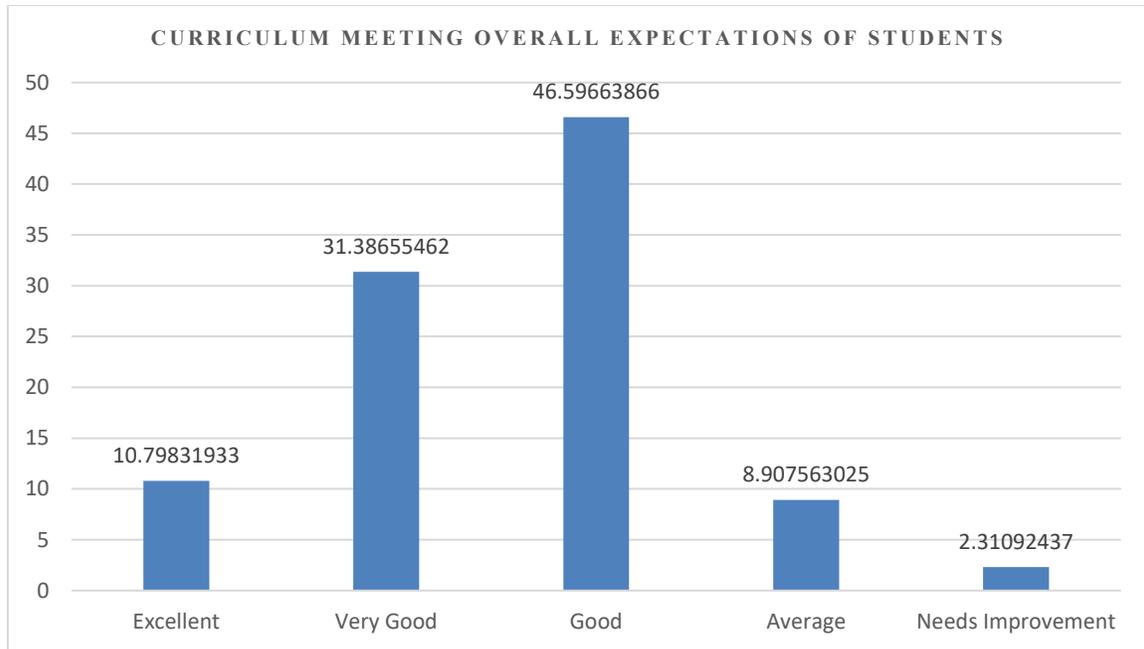
The above graph is a representation of the feedback responses given by the students as per the questionnaire.

The graph given below depicts the overall expectation meeting of the students from all years as far as syllabus is concerned. From the graph it can be seen that the students have given a feedback where 90% of the students are satisfied with the curriculum being offered. However, when the


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general comments and suggestions were analyzed, the following were the main points given by the students

- Industry used tools to be used more in teaching learning pedagogy
- More hands-on training is required in analytical courses.
- Require more industrial visits



Faculty Feedback on Curriculum

Faculty members are the backbone of any higher education institution and their feedback is very important to analyze the curriculum and to update it as per the necessity. As a practice, the department takes feedback from every course handling faculty member and the below section is an analysis of the same.

The questionnaire floated with 29 faculty members concentrated on the below questions and also on suggestions/ recommendations for the courses handled by them in the even semester of 2024-25 and odd semester of 2025-26. The synopsis of the same is given below

Question
Does the course curriculum fulfill your expectations
Does the curriculum create any interest to pursue Research/Development in the particular topic for the students?
Does the syllabus cater to industry and global needs? If no, then specify the technologies/topics to be added to make it more updated


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Few of the suggestions for the course handling faculty members is as given below

Sl No	UG/PG	Course Name	Course Code	Recommendations	Recommended by
1	UG	Basic Electronics	EC233P	Rearrangement of topics across the modules to align with the practical delivery.	Dr. Vinay Jha Pillai
2	UG	Biology for Engineers	BS435P	Rearrangement of topics across the modules to align with the practical delivery.	Dr. Hari murthy
3	UG	Network Analysis and Synthesis	EC332	Modification in course objectives, course outcomes, modifications in unit 1 and unit 2 to avoid redundancy and include relevancy to the module topic.	Dr. Mahipathi Ashoka Chakravarthy
4	UG	Electronic Devices and Circuits	EC333P	Modification in Course Outcomes, Module Contents, Unit name to avoid redundancy and include relevancy to the module topic and few experiments are added to gain insight on active filter experiments using IC741.	Dr. Gokuraju Thriveni
5	UG	Digital Signal Processing	EC532P	Replaced experiments using TMS320C6713 DSP processor with advanced DSP processors.	Dr. Chidambaram S
6	UG	Discrete Time Signal Processing	ELC532P	Modification in course objectives, course outcomes and unit 5 that deal with multi-rate signal processing. In practical, experiments related to Simulink is replaced instead of older experiments. Additional text book is added.	Dr. Chidambaram S



7	UG	Embedded Computing & Iot	EC633	This course is replaced with the available course: Cryptography and Blockchain since its modules are overlapping with honour's subject.	Dr. Arunraja A
8	PG	IC Process Technology And Cmos Vlsi Design	MTVL144E01	Modified the course title, since CMOS VLSI Design has already been studied by the students and modified all units to avoid redundancy and be more relevant to the title. Replaced older textbooks with new one.	Dr. Sourav Roy and Dr. Jyotirmoy Pathak
9	PG		MTVL143E02	Modification and rearrangement across all the units to avoid redundancy and to follow the order from designing to fabrication.	Dr. Sourav Roy and Dr. Jyotirmoy Pathak

Feedback from Alumni, Industry and Parents

In addition to the above feedback collected from faculty members, feedback was also collected from alumni, employers and parents. The major suggestions given by these stakeholders are as follows

1. Inclusion of more hands-on training in the trending areas like VLSI and Technology.
2. More facility setup at the campus related to the software and technologies that encourage VLSI and Embedded systems.

This analysis report on all the feedback collected from the students, faculty members, alumni and verticals shall be presented to the Department CDC for discussion and deliberation to be recommended to the Department Board of Studies for the academic year 2026-27 to be held in the month of March 2026.



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Action Taken Report on Curriculum Feedback Analysis

Stakeholder	Major feedback	Action Taken
Students	Focus on Industry demands	<ul style="list-style-type: none"> • Courses tailored to industry demands are introduced, with instructors from L&T EduTech teaching industry specific content. • All the PCC subjects now have LCA components and practical based subjects mandatorily have mini project.
Alumni	Prepare students for the industry through internships and projects	<ul style="list-style-type: none"> • Flexibility in the curriculum to accelerate and dedicate the final semester for internships with stipend.
Employers	Courses to be revised and include trending areas	<ul style="list-style-type: none"> • Rename the 3rd semester course as follows: <ol style="list-style-type: none"> a. Digital Electronics using Verilog - For ECE b. Digital System Design using Verilog - For EECS c. Digital Logic Design using Verilog -- For BTech ECE in VLSI <p>It will be good to expose the students the introduction to the Verilog HDL language in the 2nd year.</p> • Rename the 4th semester course on "Systems and Signal Processing" to "DSP System

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		<p>Design using Simulink" under BTech for ECE in VLSI.</p> <ul style="list-style-type: none"> • Rename the 6th semester course on "Artificial Intelligence with Python" to "Applications of Artificial Intelligence" under BTech ELCS. • Students looking for internships in the final year may work on industry relevant problem statements as listed in this website - https://github.com/mathworks/MATLAB-Simulink-Challenge-Project-Hub
Faculty	Course modifications are suggested to stay relevant to the industry.	<p>Courses such as</p> <ul style="list-style-type: none"> • Basic Electronics • Biology for Engineers • Network Analysis and Synthesis • Electronic Devices and Circuits • Digital Signal Processing • Discrete Time Signal Processing • EMBEDDED COMPUTING & IOT • IC PROCESS TECHNOLOGY AND CMOS VLSI DESIGN • DIGITAL SYSTEM DESIGN USING VERILOG <p>are modified based on the suggestions received from the faculty.</p>
Parents	Satisfied with the curriculum and facilities	Noted



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