

**Faculty of Engineering
Department of Electronics and Communication Engineering**

Curriculum Feedback Analysis - 2017-18

The feedback from the faculty members and students for the UG and PG programmes of Department of Electronics & Communication Engineering to be considered for curriculum revision for the academic year 2017-18 are summarized below.

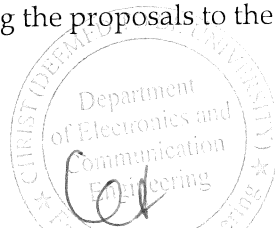
Subject Handling Faculty Feedback

- Electronic Devices and Circuits - I (EC333) - Lab Experiments need to be changed and mapped based on the theory modules. Also biasing networks of BJT and UJT can be included in the laboratory.
- Digital Electronics (EC334) - Addition of K-map in Unit-I was suggested.
- Electronic Circuits-II (EC434) - Lab experiments as suggested in the annexure can be included.
- Microcontroller & Desktop Processors (EC534) - Suggestion to separate microcontroller and processor was given.
- VLSI Design (EC631) - Fundamentals of Low Power VLSI can be included.
- Digital Communication (EC636) - BER calculation for all the modulation schemes can be introduced.
- Wireless Networks (EC831) - Existing syllabus is vast and scattered. Needs to be re-structured and the points in the annexure to be discussed.
- Applied Mathematics for Electronics Engineers (MTEC131) - Modifications in the syllabus based on the shortcomings faced while handling in the previous semester. The points included in the annexure to be discussed.
- Wireless Sensor Networks (MTEC331) - Introduction of a formal lab using a simulation tool was proposed.
- Informal labs were suggested to be included in various courses, the feasibility of which needs to be discussed.

Student and Alumni Feedback

- Introduction of more industry/application oriented subjects to the elective basket for UG and PG.
- More practical sessions for the subjects in the curriculum

These points are to be considered for discussion in the Department CDC meeting and expected to arrive at a final conclusion regarding the proposals to the BOS meeting for curriculum revision based on the feedbacks received.





FACULTY OF ENGINEERING,

Department of Electronics and Communication Engineering

Board of Studies
 03, February, 2018

Attendance Sheet

Sl.No	NAME OF THE MEMBER	SIGNATURE
INTERNAL MEMBER		
1	Dr. Iven Jose (Chair Person)	
2	Inbanila K	
3	Dr.Suganthi.S	
4	Dr.Sujatha.S	
5	Aneesh.V	
6	Chandra Mukherjee	
7	Delson.T.R	
8	Kishorekumar R	
9	Partha.R	
10	Shashikumar D	
11	Sushanth.G	
12	Vinay Jha Pillai	
13	Xavier Arockiaraj	

Sl.No	NAME OF THE MEMBER	SIGNATURE
EXTERNAL MEMBER		
1	Dr.G.Sadashivappa	
2	Dr.Ravish Aradhya.H.V	
3	Dr.Sujatha.J	

Minutes of the 9th Meeting of the Board of Studies of the Department of Electronics and Communication Engineering Held on 03 February 2018 at 9.30 am at the Conference Room, First Block, Kengeri Campus, CHRIST (Deemed to be University)

In the Chair: Dr. Iven Jose, Associate Dean

Members Present:

All members in the attendance list (Annexure A) were present.

Declaration of Quorum and Calling the Meeting to Order:

The Chairperson commenced the meeting with a silent prayer followed by a note of welcome to all the members. Thereafter the Chairperson declared the validity of the Quorum and called the Meeting to Order.

Matters on the Agenda:

1. **To confirm the Minutes of the previous meeting held on 20 January, 2017**
The minutes of the previous meeting of the Board of Studies was duly reviewed and approved by the meeting. It was noted that there were no matters arising out of the minutes.
2. **To ratifying the circular resolution passed on 16 October 2017.**
Circular resolution was passed on 16 October 2017 on the policies for the full time industry mode project (EC871) with stipend in eighth semester for the students having consistent academic record with no backlogs. The Board ratified the circular resolution.
3. **To consider and recommend the changes in the curriculum for the following programmes**

Following changes were suggested for inclusion in the structure (Annexure B).

A. BTech (Electronics and Communication Engineering)-Proposals(as per Annexure B)

1. Inclusion of Power Electronics as a core subject by merging EC333-Electronics Devices and Electronic Circuits I and EC434-Electronic Circuits II.
2. To shift the content of 8051 Microcontroller into EC432- Computer organization and Architecture from EC534-Microcontroller and Real time embedded system.
3. Advanced Microprocessor to be removed from elective basket.
4. Suggestions were given to include one more subject on VLSI concentrating on VLSI testing, Microelectronics, PCB design, EMI/EMC testing and Impedance matching.
5. Suggestions were given to include one more subject for Communication concentrating on switching circuit, latest technologies in RF, MIMO, smart antennas, GPS and GPRS.

Actions: After due review the proposal was approved by the Board.

B. MTech (Communication Systems)-Proposal (as per Annexure C)

1. The experts strongly suggested to modify the course structure of M.Tech. The programme should offer only 10 courses for 2 semester and the final year of the programme should be project work.

Actions: After due review the proposal was approved by the Board.

4. **To consider and approve the Certificate Programmes**

Sl. No.	Course	Credit	hours
1	CCIO (Certified Chief Innovation Officer-Rocheston)	3	45

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Actions: After due review the proposal was approved by the Board

5. Introduction of CIMA- (Chartered Institute of Management Accountants) In Undergraduate programme as Certificate /Elective Course.

The Board appreciated this Initiative and recommended introduction of this course during early Semesters.

6. To consider and approval of the panel of examiners and BOS Experts

The board discussed and approved a panel of 18 examiners and 6 BOS experts (as per Annexure D).

7. To consider and approval of the selection criteria, admission process for the BTech and MTech programmes

The Board approved the selection and admission criteria adhered by the department for the BTech and MTech programmes applicable from 2018-19.

8. QIP & FDP details for approval

The board approved

S.No	Programme	Title	Duration
1	QIP	Internet of Things	5 days, May 2018
2	QIP	Research tools	3 days, Oct 2018
3	FDP I	As decided by Institute	2 days, May 2018
4	FDP II	As decided by Institute	2 days, Nov 2018

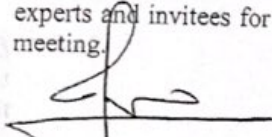
9. Research and Publication

1. All M.tech students should have conference and journal publications as an outcome of their project work
2. 30% of the UG project to result in conference publications.
3. All faculty members to have atleast one publication every year in Scopus indexed Journal
3. To attain minimum one external funded project and patent in this academic year

10. To consider any others matters with the permission of the Chair.

The Board Discussed the National Board of Accreditation (NBA) processes for the accreditation of the BTech and MTech programme. It also discussed the Vision, Mission and Programme Educational Objectives (as per Annexure E) of the department. After the due deliberations the Board approved the Vision, Mission, and Programme Educational Objectives of the Department.

With no other matters to discuss Chairperson thanked all the board of studies members including experts and invitees for their presence and valuable suggestions. The Chairperson adjourned the meeting.



Dr Iven Jose
Chairperson
Board of Studies

17th Dec 2018

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Faculty of Engineering
Department of Electronics and Communication Engineering
Action Taken Report on Syllabus 2017-18

Department of Electronics and Communication Engineering has made it mandatory to collect feedback from all the stakeholders for revision of curriculum. This feedback is later analyzed and discussed in the Curriculum Design and Development Cell (CDC) at the department to ensure that the feedback is taken positively and implemented for the next academic year. This academic year of 2017-18, feedback were collected from various stakeholders like students, teachers, alumni and industry experts along with parents. These feedback were then analyzed and deliberated in meetings within the department. Through these discussions and deliberations, a feasibility analysis was performed and the following are the actions taken based on the same. This will be implemented for the academic year of 2018-19.

Action Taken

Action taken as per the suggestions of the stakeholders and department BOS, for the academic year 2018-19 is as follows:

1. Shift the content of 8051 Microcontroller into EC432- Computer organization and Architecture from EC534-Microcontroller and Real time embedded system.
2. Advanced Microprocessor to be removed from elective basket.
3. All M.Tech students should have conference and journal publications as an outcome of their project work
4. 30% of the UG project to result in conference publications.
5. All faculty members to have at least one publication every year in Scopus indexed Journal
6. To attain minimum one external funded project and patent in this academic year

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